

NOT (1001) Instruction Cycle

15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
1001				DR			SR			111111					

Instruction Fetch

MAR $\leftarrow$ **PC**

PC $\leftarrow$ **PC** + 1

MDR $\leftarrow$ **memory**[**MAR**]

IR $\leftarrow$ **MDR**

Decode

IR[15:12] **decoded**

Evaluate Address

n/a

Operand Fetch

ALU_{INA} $\leftarrow$ **R**_{IR}[8:6]

Execute

ALU_{OUT} $\leftarrow$ **not** **ALU**_{INA}

Store Result

R_{IR}[11:9] $\leftarrow$ **ALU**_{OUT}